# ATENT COOPERATION TREATY

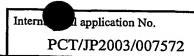
# PCT 10/518564

## INTERNATIONAL PRELIMINARY EXAMINATION REPORT

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INTERNATIO	ONAL PRELIMINARY	EXAMIN	ATION REP	ORT		
	(PCT Article 36 and	l Rule 70)				
Applicant's or agent's file reference PNDF-03034	FOR FURTHER ACTION	See Notific Preliminary	cation of Tra Examination Re	nsmittal of Internationa port (Form PCT/IPEA/416)		
International application No. PCT/JP2003/007572	International filing date (day/m 13 June 2003 (13.06.			day/month/year) 2002 (20.06.2002)		
International Patent Classification (IPC) or na H04B 7/10				2002 (20:00:2002)		
Applicant	NEC CORPORATI	ION				
This international preliminary examinant and is transmitted to the applicant age.	nation report has been prepared	hy this Intern	ational Prelimina	Tramining Authority		
and is transmitted to the applicant act	ording to Article 36.			ary examining Authority		
2. This REPORT consists of a total of6 sheets, including this cover sheet.						
which does not the basis tol	d by ANNEXES, i.e., sheets of this report and/or sheets contain	nna rectificat	n, claims and/or ions made befor	drawings which have been re this Authority (see Rule		
70.16 and Section 607 of the Administrative Instructions under the PCT).  These annexes consist of a total of sheets.						
3. This report contains indications relations	. This report contains indications relating to the following items:					
I Basis of the report						
II Priority						
III Non-establishment of	Non-establishment of opinion with regard to novelty, inventive step and industrial applicability  Lack of unity of invention  Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
IV Lack of unity of inver						
v Reasoned statement u citations and explanat						
VI Certain documents cit	ed					
VII Certain defects in the	nternational application					
VIII Certain observations of	n the international application					
Date of submission of the demand	Date of c	completion of	this report			
13 June 2003 (13.06.200	)3)	30 M	arch 2004 (30	0.03.2004)		
Name and mailing address of the IPEA/JP	Authoriz	ed officer				
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Form PCT/IPEA/409 (cover sheet) (July 1998)





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	of the report		
1. With	regard to the elements of the international application:*		·
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	the claims:		
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	the drawings:		
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2. With the in These	regard to the language, all the elements marked above were ava ternational application was filed, unless otherwise indicated unde elements were available or furnished to this Authority in the foll	ailable or furnished	d to this Authority in the language in which
	the language of a translation furnished for the purposes of intern	owing language	which is:
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3. With prelim	regard to any nucleotide and/or amino acid sequence dininary examination was carried out on the basis of the sequence li	sclosed in the in isting:	iternational application, the international
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	and the supplemental B	oox (Rule 70.2(c))."	.**
and 70	ement sheets which have been furnished to the receiving Office i report as "originally filed" and are not annexed to this re .17).	sport since mey a	ao noi contain amenaments (Rule 70.16
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v.	Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement

1.	Statement			
	Novelty (N)	Claims	1-13	YES
		Claims		NO
	Inventive step (IS)	Claims		YES
	•	Claims	1-13	NO
	Industrial applicability (IA)	Claims	1-13	YES
	·	Claims		NO

#### 2. Citations and explanations

Document 1: CD-ROM of the specification and drawings annexed to the written application of Japanese Utility Model Application No. 24752/1992 (Laid-open No. 84884/1993) (Mitsubishi Electric Corp.), 16 November 1993

Document 2: JP 2002-135034 A (NEC Corp.), 10 May 2002

Document 3: JP 2001-332925 A (NEC Corp.), 30 November 2001

Document 4: JP 2001-272447 A (Koden Electronics Co.,

Ltd.), 5 October 2001

Document 5: JP 2001-251124 A (NEC Corp.), 14 September 2001

### Claims 1, 4, 5, and 12

Document 1 (se paragraphs [0028] to [0033]) discloses an array antenna receiver device that performs a correction process, wherein values for the phase and amplitude of each antenna element are calculated, an antenna element for which said values exceed a given range is determined to have failed, said antenna element determined to have failed is placed in a non-operating state, and the correction process is carried out using the remaining antenna elements.

The array antenna receiver device disclosed in document 1 differs from the present application in that (1) a branch for which the SIR value of a received correction signal does not exceed a specified threshold value is not determined to have failed, and (2) correction is not performed separately for each receiving branch.

However, concerning the above point (1), an array antenna receiver device that performs a correction process, wherein a branch for which the SIR value of a received correction signal does not exceed a specified threshold value is determined to have failed is known in the art, as disclosed in document 2 (see paragraphs [0049] to [0071] and fig. 1), and thus, applying the constitution disclosed in document 2 in place of the failure detection constitution disclosed in document 1 is not recognized as posing any particular difficulty for a person skilled in the art.

Further, concerning the above point (2), an array antenna receiver device that performs a correction process, wherein the correction process is performed separately and independently for each of a plurality of receiving branches is known in this technical field, as disclosed in document 3 or document 5 (or in document JP 11-46180 A, disclosed in the prior art section of the present application (and which is also cited as prior art for documents 3 and 5)), for example, and thus, no particular difficulty is recognized.

Moreover, the present invention performs a separate correction process for each receiving branch using a means for storing a predetermined reference modulation result for each receiving branch, and a means for detecting a correction amount for amplitude/phase data based on the reference demodulation result, but when performing a separate correction process for each branch, it is sufficient that the invention detect the receiving status

of the correction signal input to each branch and calculate the correction amount, and thus, the determination of whether to calculate the correction amount by comparing the signal prior to input and the signal after it passes through the branch, as in a conventional correction process disclosed in the above documents, or by comparing a reference stored beforehand and the signal after it passes through the branch, as in the correction process of the present invention, is nothing more than a design feature fittingly determined by a person skilled in the art, and does not pose any particular difficulty.

Therefore, a person skilled in the art could easily conceive of the inventions described in claims 1, 4, 5, and 12 in the light of documents 1 to 3 and 5, and thus, said inventions do not involve an inventive step.

#### Claims 2, 3, 7, and 9

A feature wherein interference relative to a desired wavelength is taken into consideration and the correction signal power is set to a sufficiently low fixed power level, and a feature wherein the power level for an input correction signal is controlled according to the receiving status of the correction signal are disclosed in documents 3 and 5. Further, document 3 also discloses a feature wherein the correction cycle varies depending on receiving status.

Therefore, the invention described in claims 2, 3, 7, and 9 does not involve an inventive step.

### Claims 6, 8, 10, and 13

An array antenna receiver device that performs a correction process, wherein the correction process is performed by supplying a correction signal on a timedivision basis, is a conventional device (see document 4),

and constituting the array antenna receiver device disclosed in document 1 such that the correction process is performed on a time-division basis is not recognized as posing any particular difficulty.

Therefore, the inventions described in claims 6, 8, 10, and 13 do not involve an inventive step.

#### Claim 11

Document 2 also discloses a feature wherein error rate is taken into consideration rather than SIR (see paragraphs [0072] to [0077] and fig. 2).

Therefore, the invention described in claim 11 does not involve an inventive step.